

# TD62381P, TD62381F

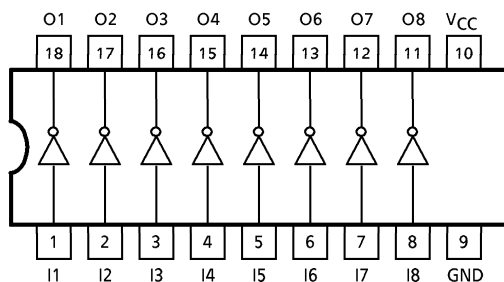
## 8CH LOW SATURATION SINK DRIVER

The TD62381P and TD62381F are comprised of eight NPN low saturation drivers. These devices are specifically designed for multiplexed digit driving of eight digit common-cathode LED and also can be employed as a sink driver for multiplexed LED displays using with the TD62785P and TD62785F at standard supply voltage, 5V. Applications include relay, hammer, lamp and LED display drivers.

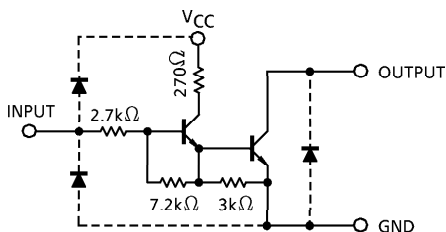
### FEATURES

- Low saturation output voltage :  $V_{CE(sat)} = 0.9V$  (Max.)  
@ $I_{out} = 500mA$
- Output rating 15V (Min.) / 500mA (Max.)
- Input compatible with TTL and 5V CMOS
- Low level active inputs
- Standard supply voltage
- Package type-P : DIP-18pin
- Package type-F : SOP-18pin

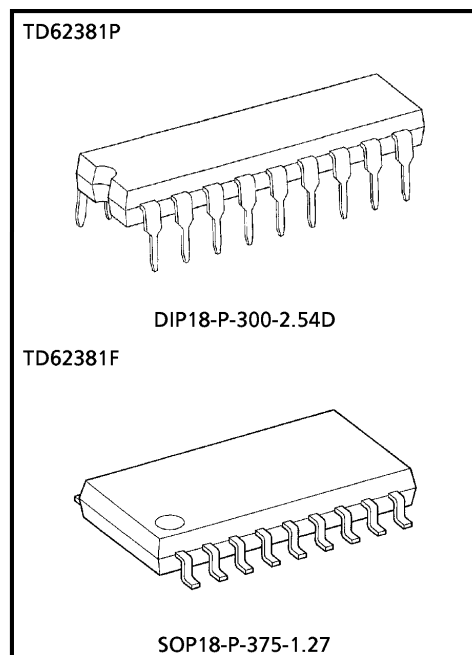
### PIN CONNECTION (TOP VIEW)



### SCHEMATICS (EACH DRIVER)



(Note) The input and output parasitic diodes cannot be used as clamp diodes.



Weight  
 DIP18-P-300-2.54D : 1.47g (Typ.)  
 SOP18-P-375-1.27 : 0.41g (Typ.)

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● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

**MAXIMUM RATINGS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	7	V	
Output Sustaining Voltage	V <sub>CE (SUS)</sub>	15	V	
Output Current	I <sub>OUT</sub>	500	mA / ch	
Input Voltage	V <sub>IN</sub>	7	V	
Input Current	I <sub>IN</sub>	5	mA	
Power Dissipation	P	P <sub>D</sub>	1.47	W
	F	(Note)	0.96	
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	

(Note) Delated above 25°C in the proportion of 11.7mW/°C (P-Type), 7.7mW/°C (F-Type).

**RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)**

CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT		
Supply Voltage	V <sub>CC</sub>	—	4.5	5.0	5.5	V		
Output Voltage	V <sub>OUT</sub>	—	—	—	12	V		
Output Current	P	DC 1 Circuit, Ta = 25°C	—	—	400	mA / ch		
							F	—
	P		T <sub>pw</sub> ≤ 25ms 8 Circuit On	—	—		400	—
	F		Ta = 85°C	—	—		400	—
F	T <sub>j</sub> = 120°C	—	—	330	—			
						Duty = 50%	—	—
Input Voltage	V <sub>IN</sub>	—	0	—	V <sub>CC</sub>	V		
	Output On	V <sub>IN (ON)</sub>	2.4	—	V <sub>CC</sub>			
	Output Off	V <sub>IN (OFF)</sub>	0	—	0.4			
Power Dissipation	P	P <sub>D</sub>	—	—	0.52	W		
	F		—	—	0.35			

**ELECTRICAL CHARACTERISTICS (Ta = 25°C, V<sub>CC</sub> = 5V)**

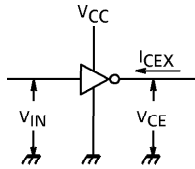
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Leakage Current	I <sub>CEX</sub>	1	V <sub>IN</sub> = OPEN V <sub>OUT</sub> = 12V, Ta = 85°C	—	—	100	μA
Output Saturation Voltage	V <sub>CE (sat)</sub>	2	I <sub>OUT</sub> = 500mA	—	—	0.9	V
			I <sub>OUT</sub> = 350mA	—	—	0.7	
Input Current	I <sub>IN (ON)</sub>	3	V <sub>CC</sub> = 5V, V <sub>IN</sub> = 2.4V	—	0.4	0.7	mA
Input Voltage (Output On)	V <sub>IN (ON)</sub>	—	V <sub>CC</sub> = 5V	—	—	2.4	V
Supply Current	I <sub>CC</sub>	4	V <sub>CC</sub> = V <sub>IN</sub> = 5V	—	—	17	mA / ch
Turn-On Delay	t <sub>ON</sub>	5	V <sub>OUT</sub> = 10V, R <sub>L</sub> = 20Ω C <sub>L</sub> = 15pF	—	0.1	—	μs
Turn-Off Delay	t <sub>OFF</sub>			—	1.2	—	μs

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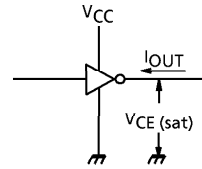
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**TEST CIRCUIT**

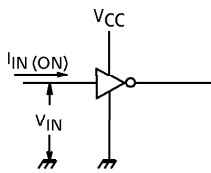
1.  $I_{CEX}$



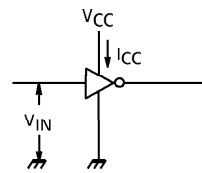
2.  $V_{CE(sat)}$



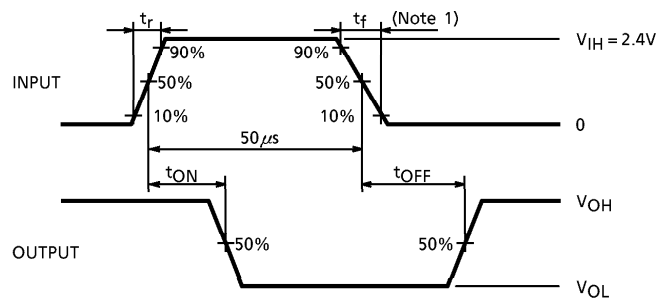
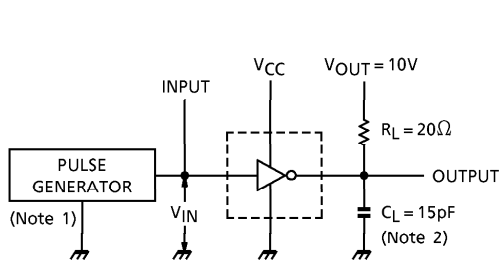
3.  $I_{IN(ON)}$



4.  $I_{CC}$



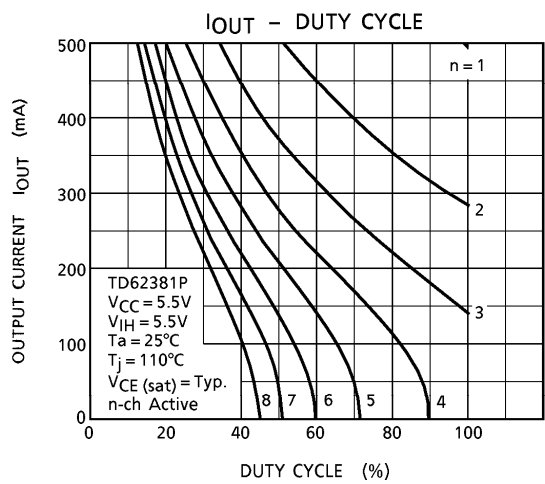
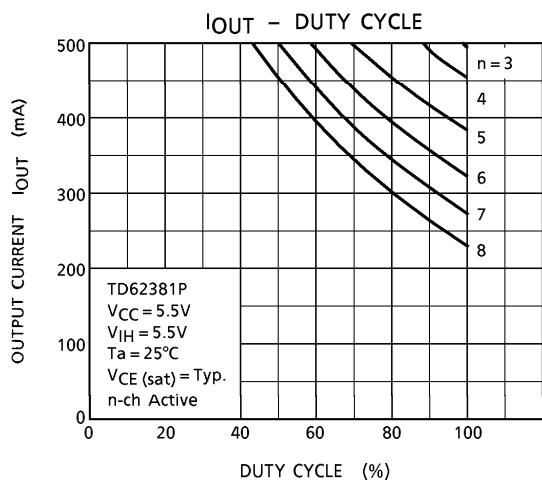
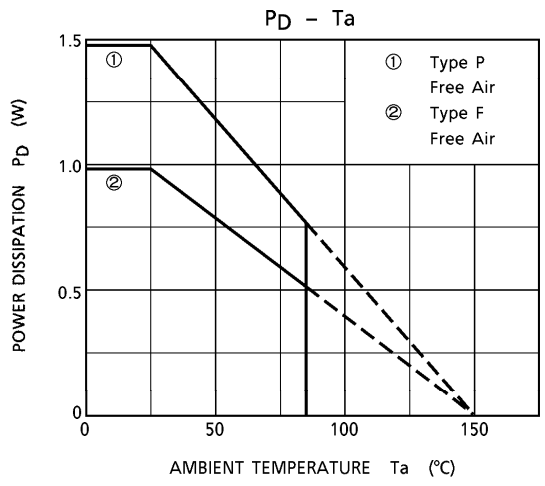
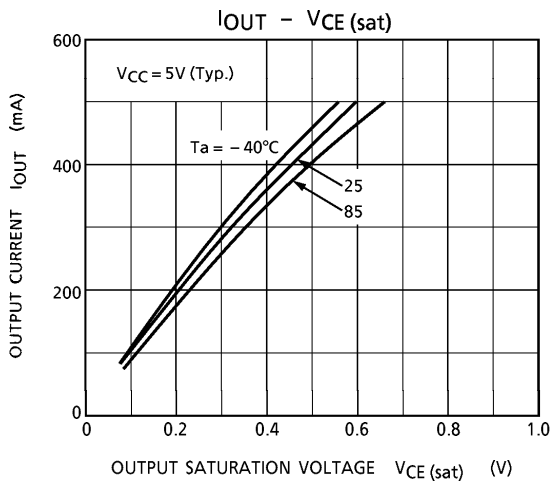
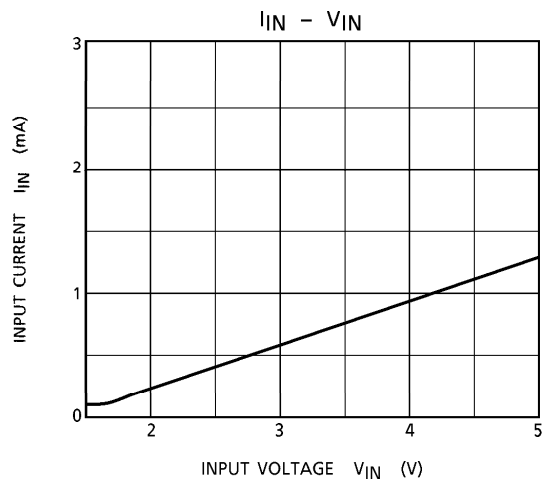
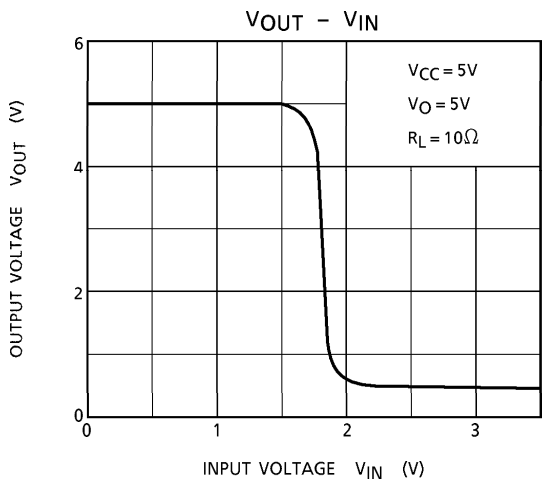
5.  $t_{ON}, t_{OFF}$

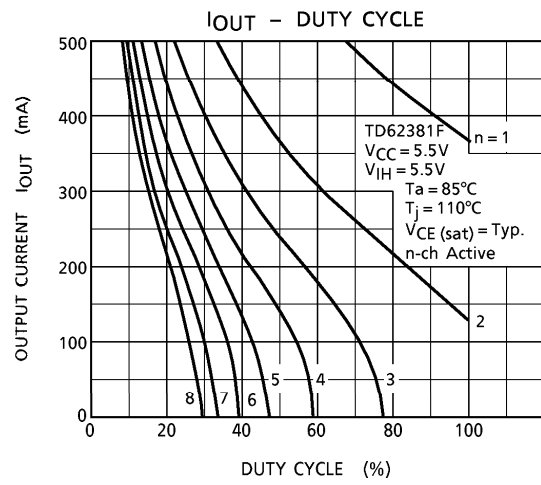
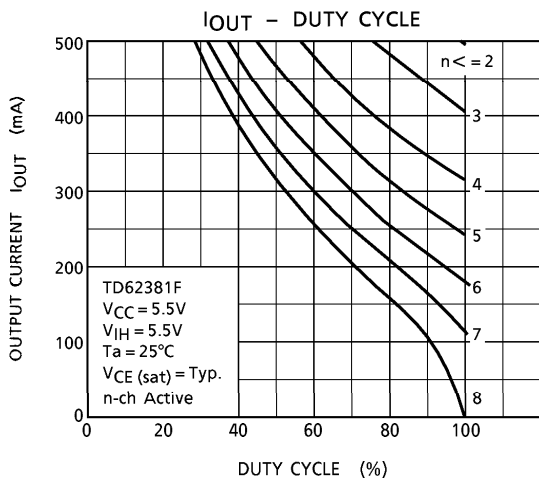


- (Note 1) Pulse Width  $50\mu s$ , Duty Cycle 10%  
Output Impedance  $50\Omega$ ,  $t_r \leq 5ns$ ,  $t_f \leq 10ns$
- (Note 2)  $C_L$  includes probe and jig capacitance.

**PRECAUTIONS for USING**

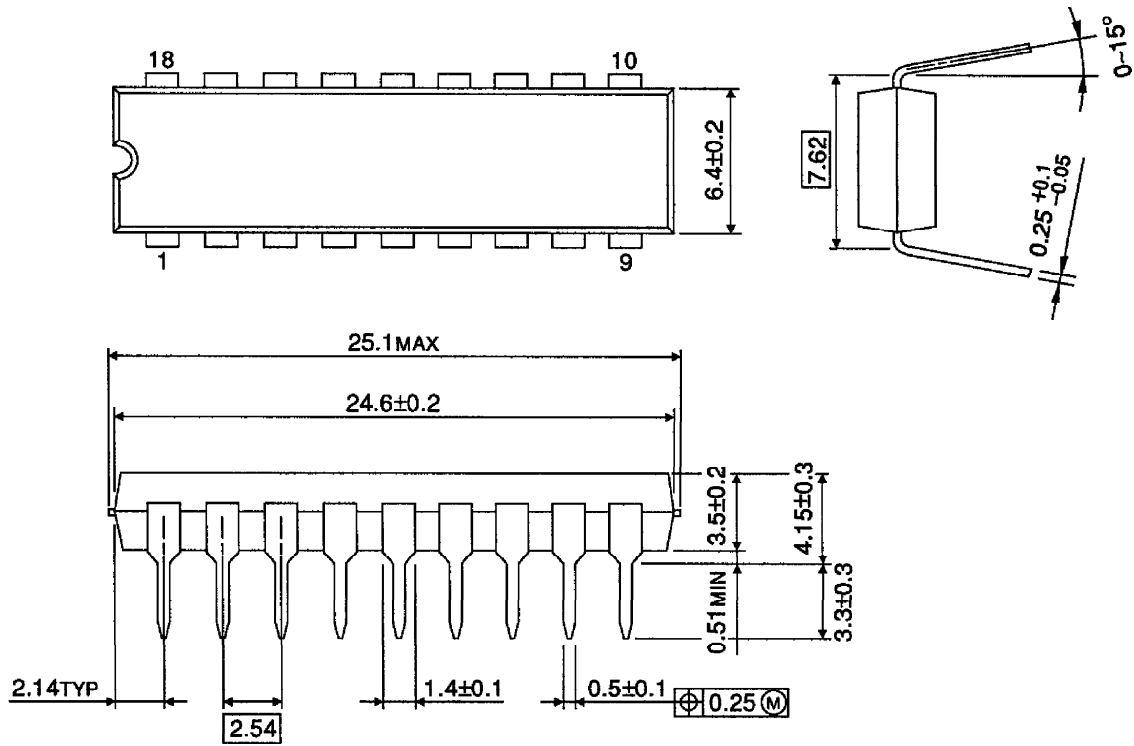
Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.





**OUTLINE DRAWING**  
DIP18-P-300-2.54D

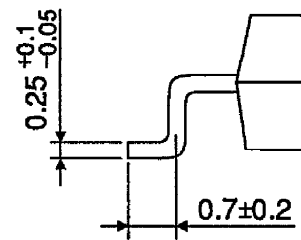
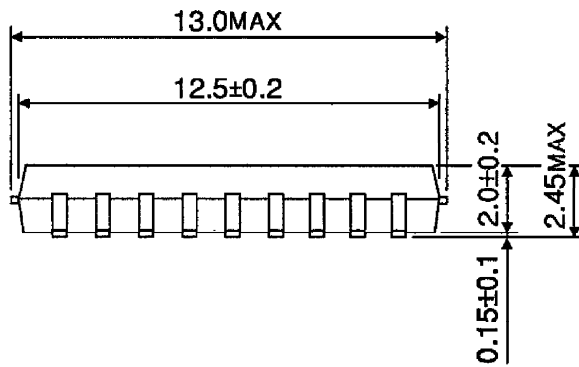
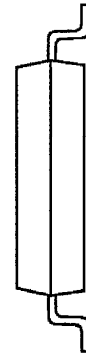
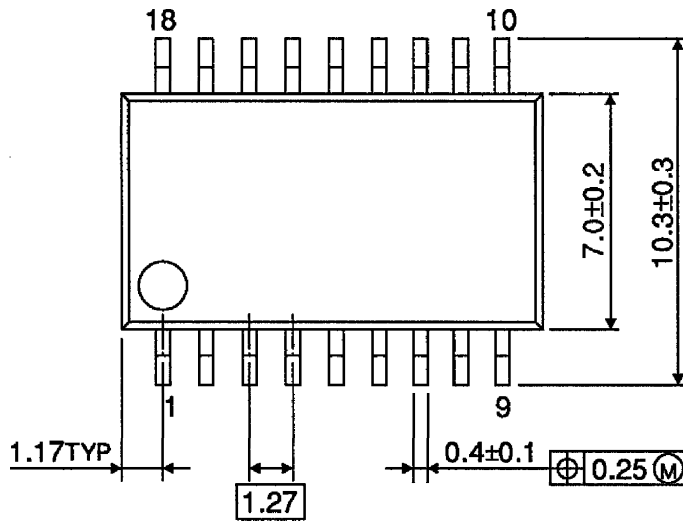
Unit : mm



Weight : 1.47g (Typ.)

OUTLINE DRAWING  
SOP18-P-375-1.27

Unit : mm



Weight : 0.41g (Typ.)